

FIG. 1

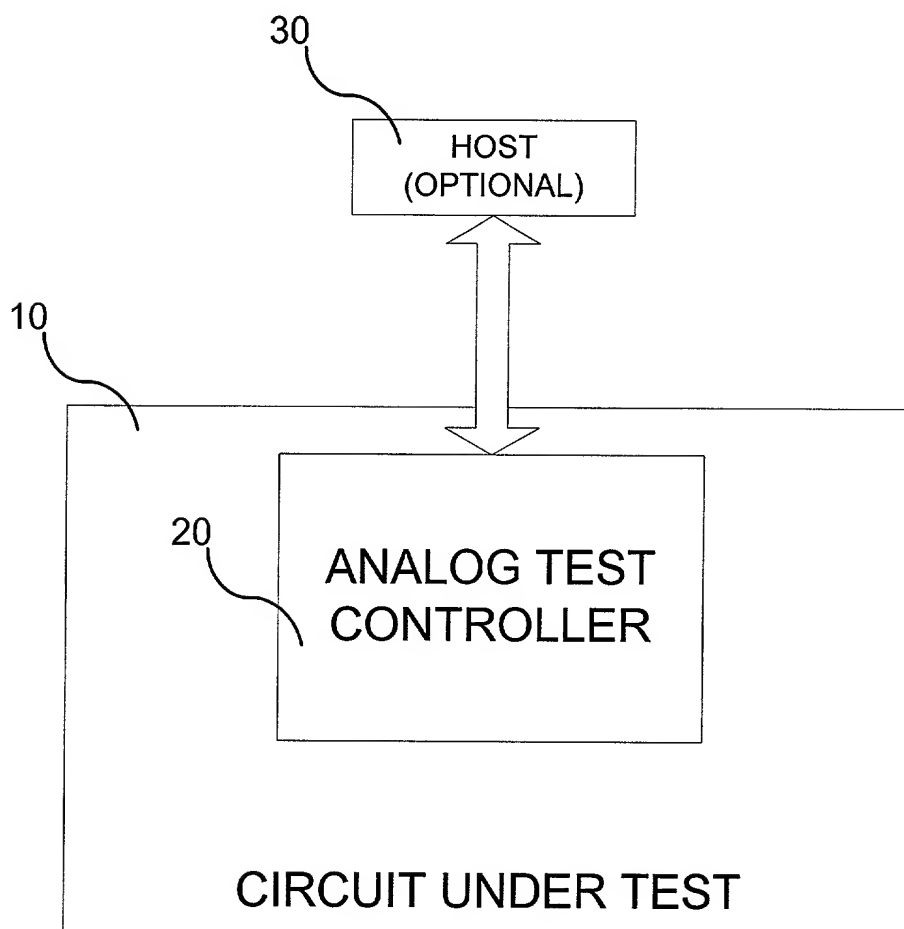


FIG. 2

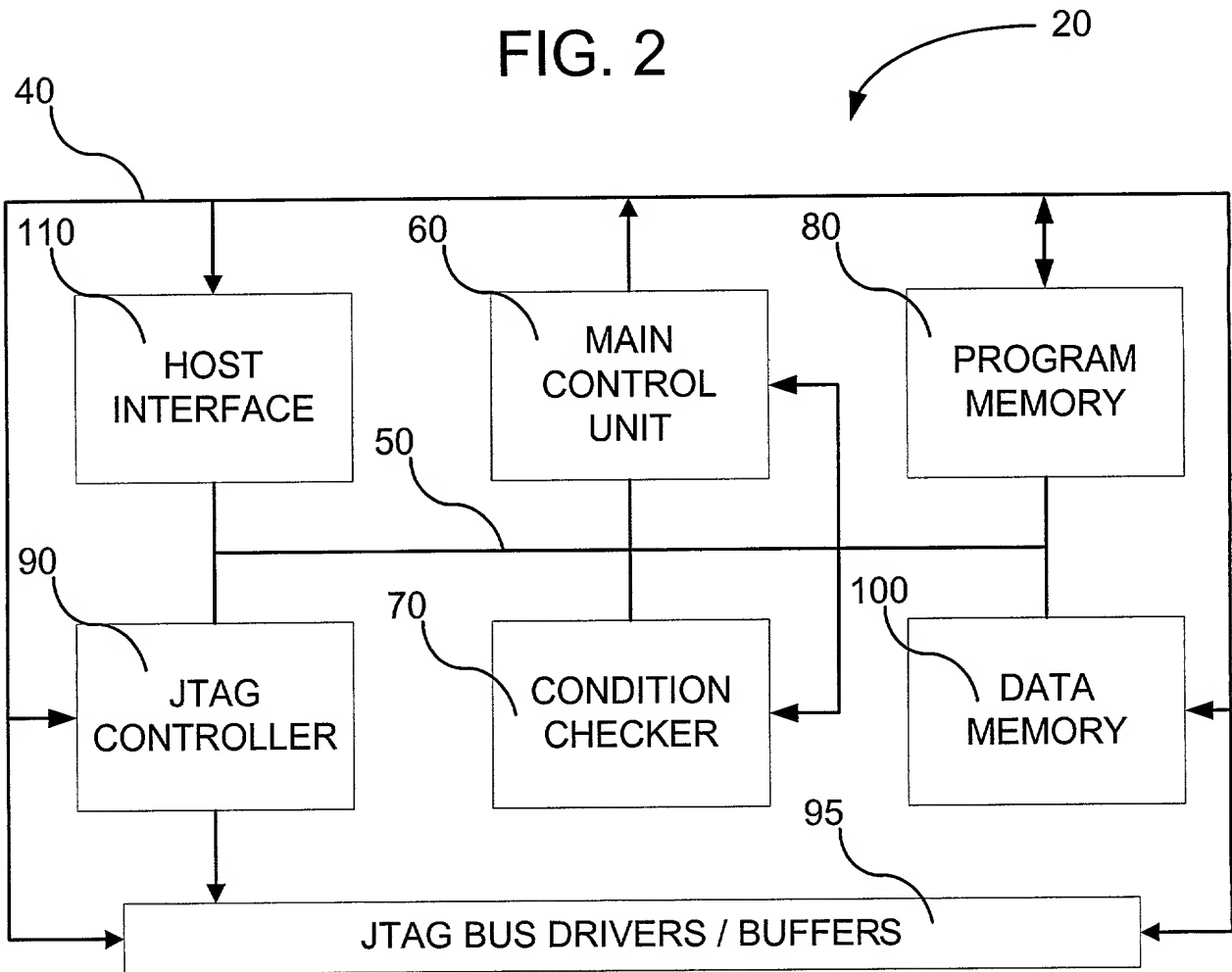
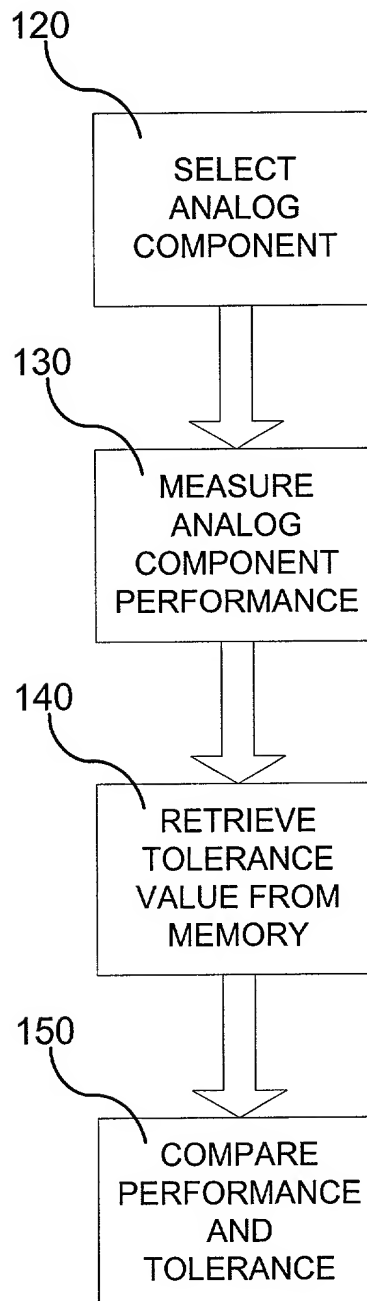
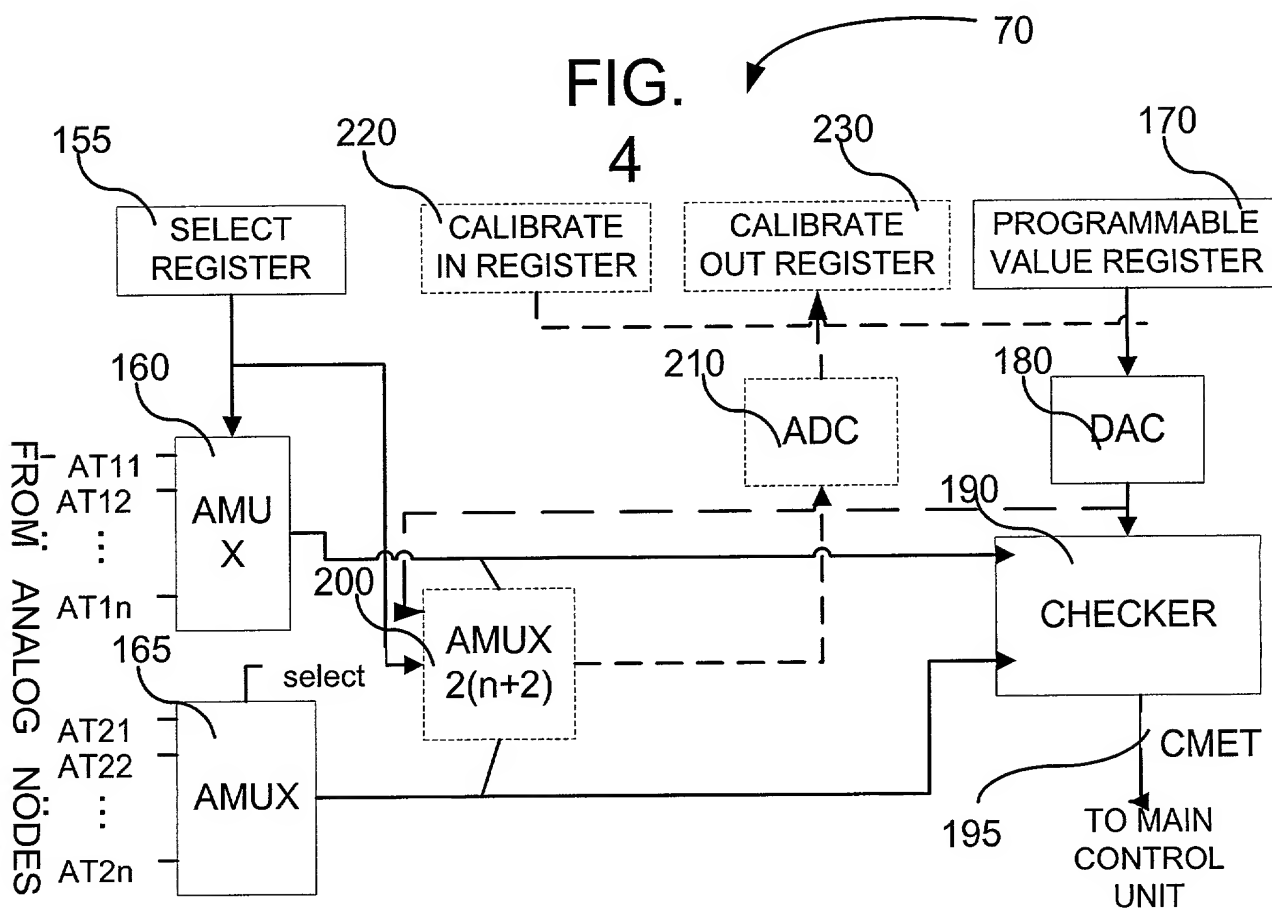


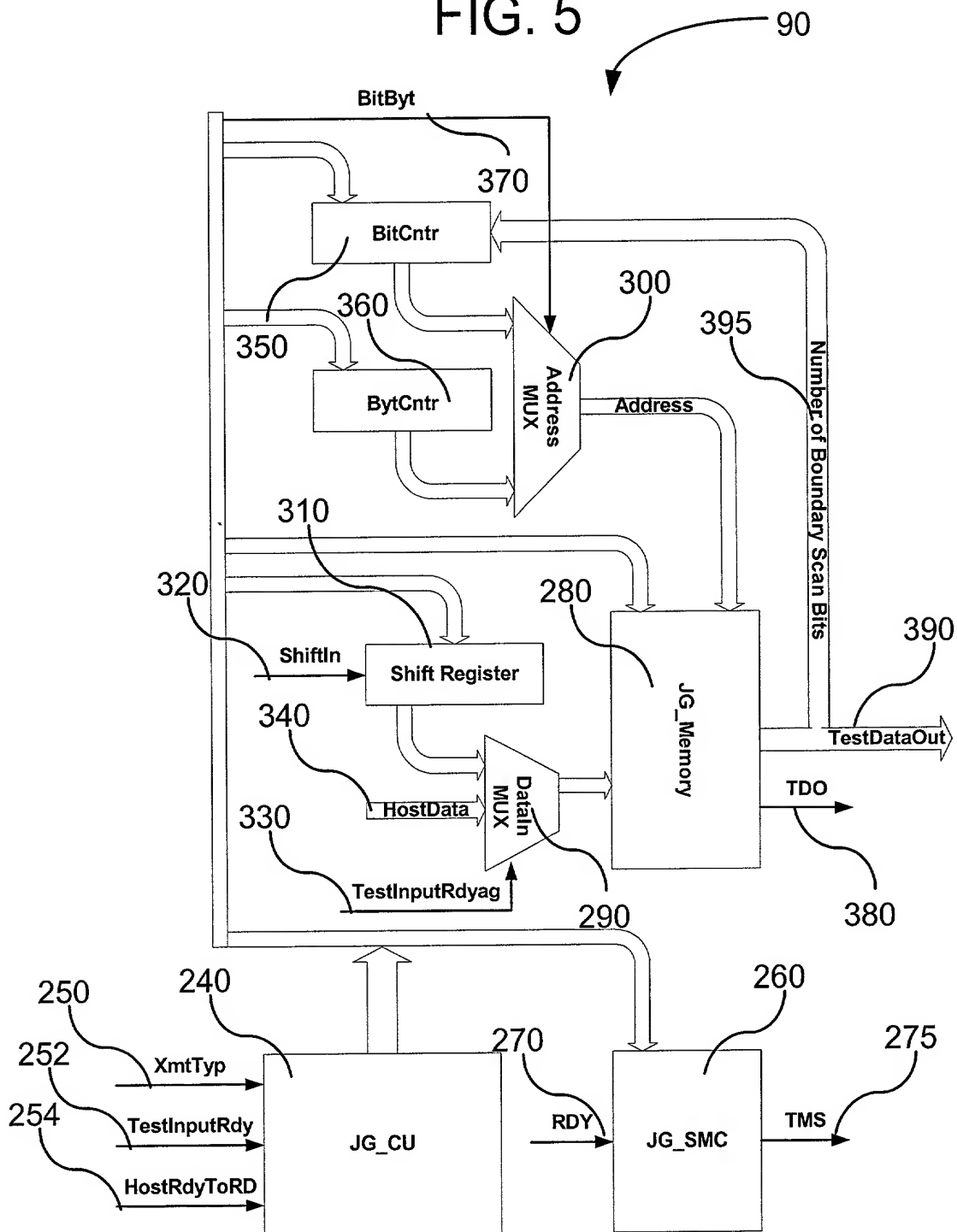
FIG. 3





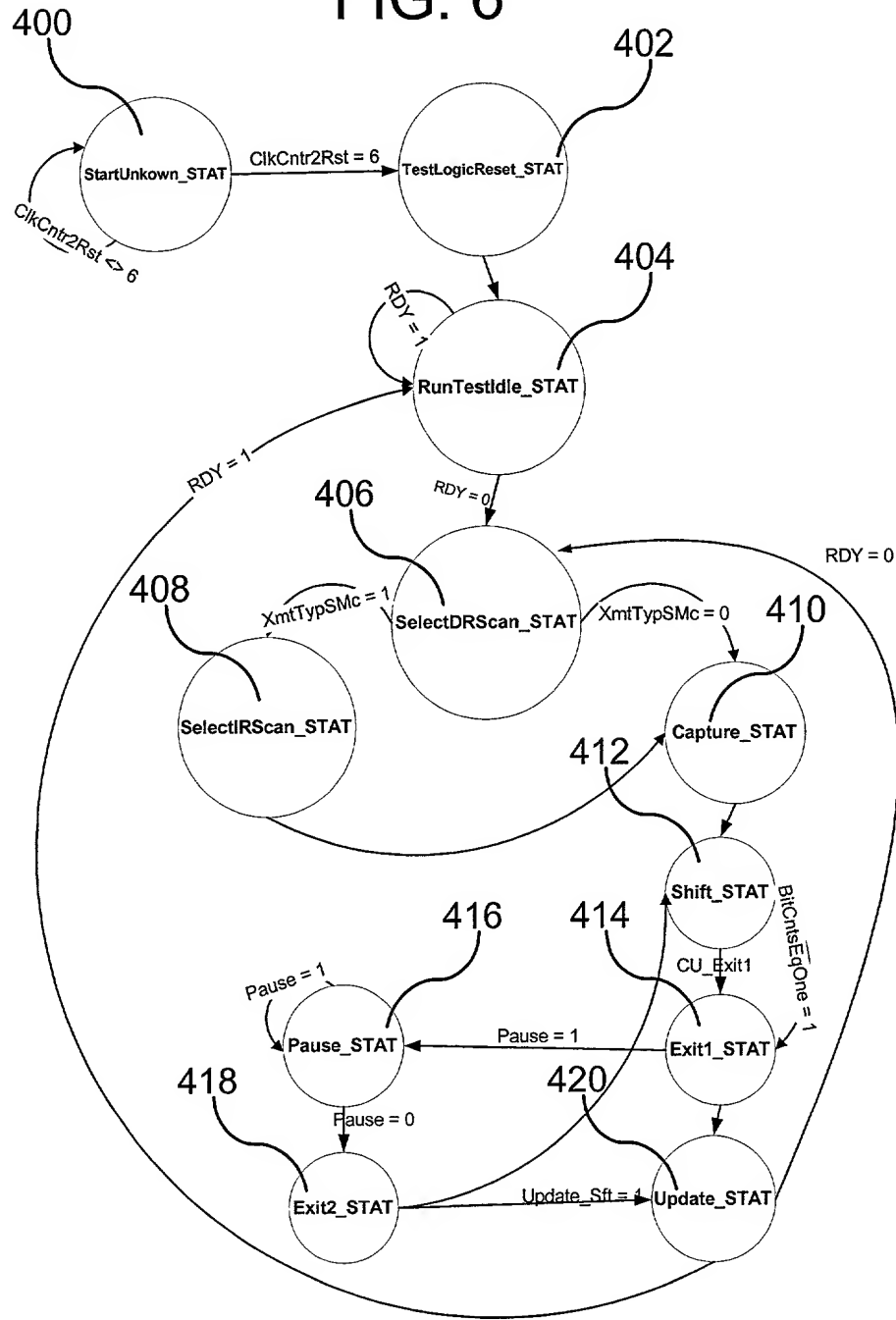
10039934-100001

FIG. 5



10039934.100601

FIG. 6



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FIG. 6a

STATE	CONTROL							OUTPUT
	R D Y	X m t T y p S M c	C U - E X I T 1	B i t C n t s E q O n e	P a u s e	U p d a t e S f t	C l k C n t r 2 R s t	TMS
StartUnknown_STAT	X	X	X	X	X	X	X	1
TestLogicReset_STAT	X	X	X	X	X	X	X	0
RunTestIdle_STAT	1	X	X	X	X	X	X	0
RunTestIdle_STAT	0	X	X	X	X	X	X	1
SelectDRScan_STAT	X	1	X	X	X	X	X	1
SelectDRScan_STAT	X	0	X	X	X	X	X	0
SelectIRScan_STAT	X	X	X	X	X	X	X	0
Capture_STAT	X	X	X	X	X	X	X	0
Shift_STAT	X	X	1	X	X	X	X	1
Shift_STAT	X	X	X	1	X	X	X	1
Shift_STAT	X	X	0	0	X	X	X	0
Exit1_STAT	X	X	X	X	0	X	X	1
Exit1_STAT	X	X	X	X	1	X	X	0
Pause_STAT	X	X	X	X	0	X	X	1
Pause_STAT	X	X	X	X	1	X	X	0
Exit2_STAT	X	X	X	X	X	1	X	1
Exit2_STAT	X	X	X	X	X	0	X	0
Update_STAT	0	X	X	X	X	X	X	1
Update_STAT	1	X	X	X	X	X	X	0

FIG. 6a

FIG. 7

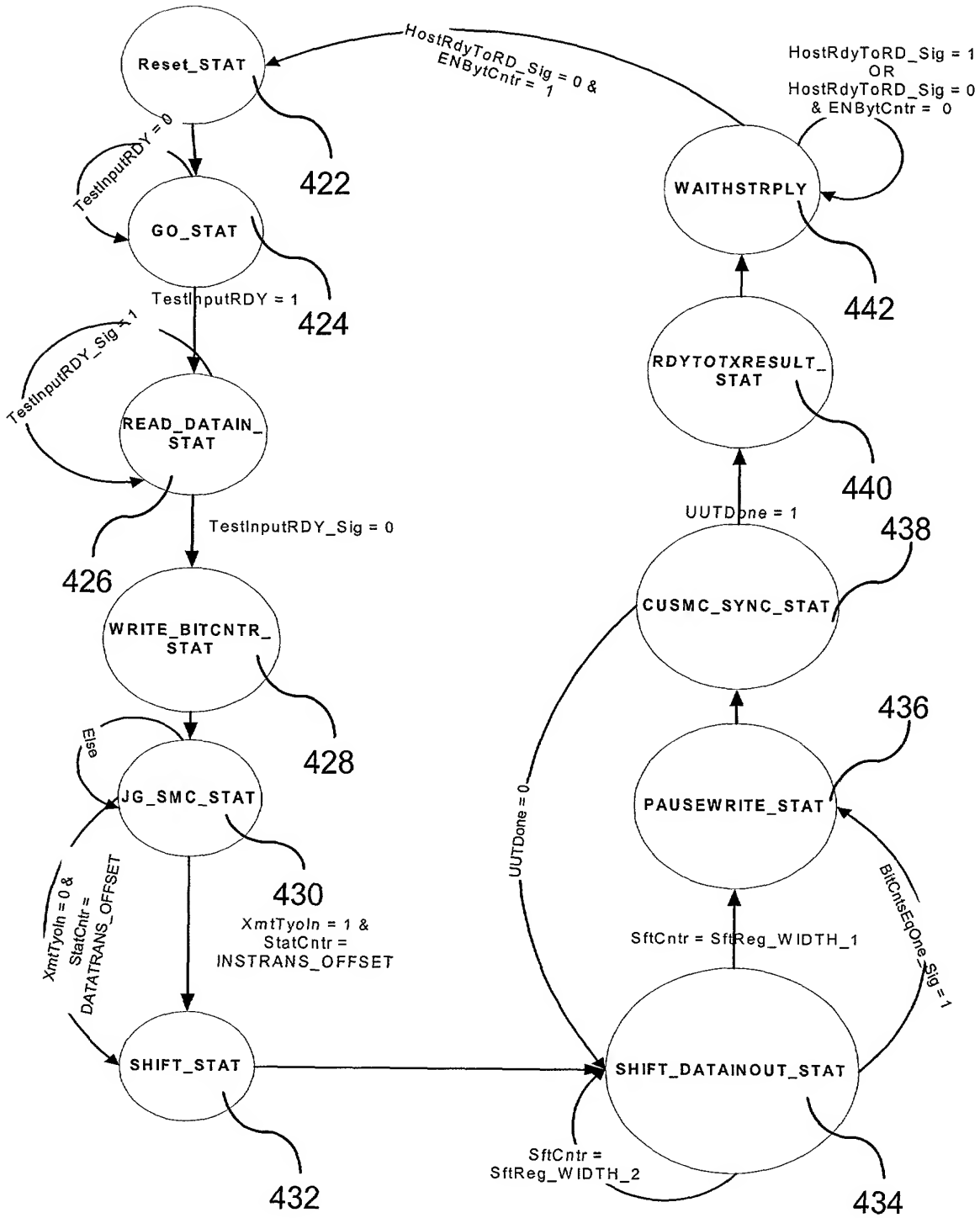
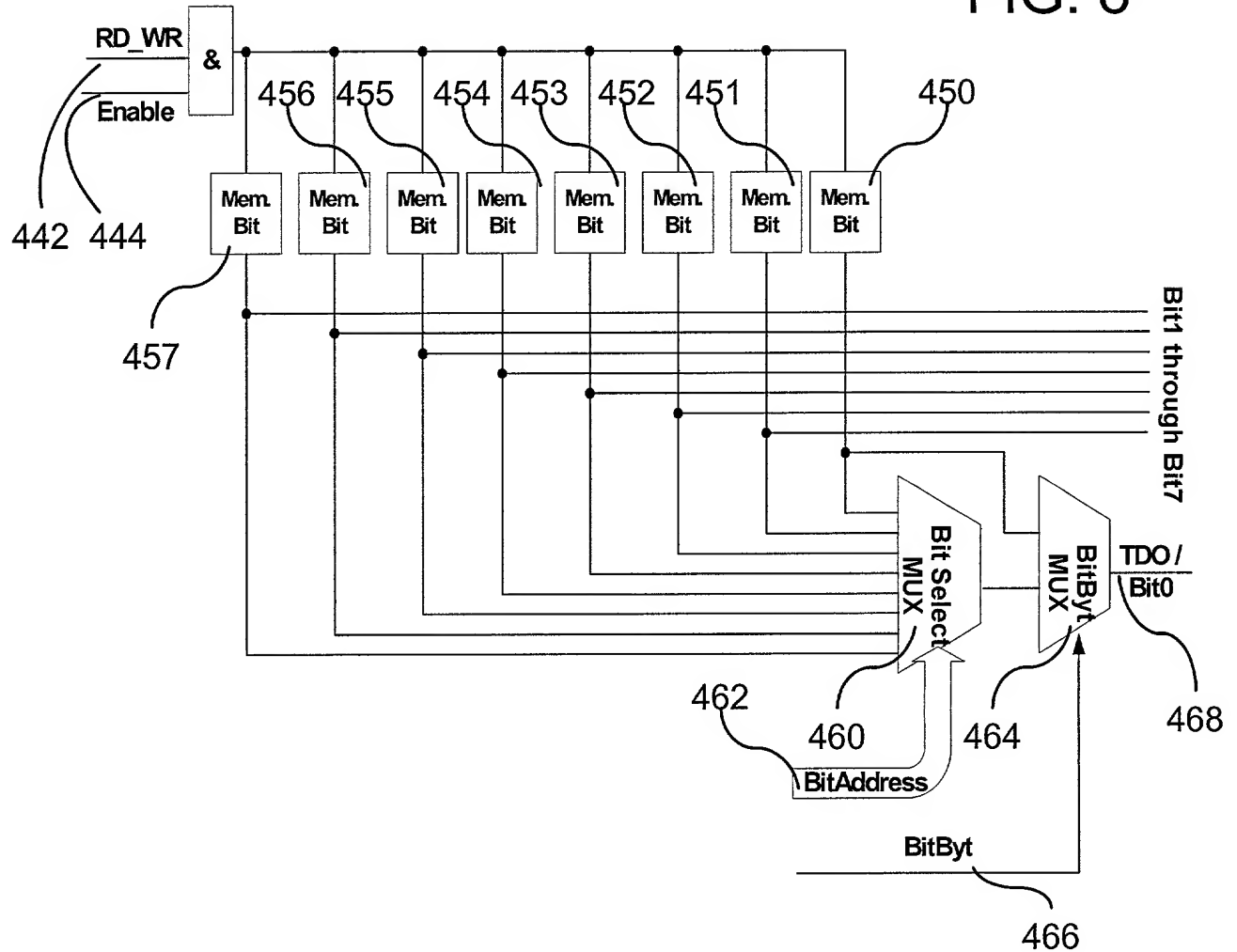


FIG. 7

FIG. 7A

STATE/CONTROL OUTPUT	Test Input RDY	Xm t t y p i n	StatCntrl = INSTTRANS-OFFSET	StatCntrl = DATA TRANS-OFFSET	BitCntrl = 9	SpfCntrl = SpfReg-WIDTH-12	UUTDone	HostRDYTORDB	ENBBytCntrl	RDWR	LDBitCntrl	ENBitCntrl	Pause	BitByt	TestResultRDY	ENBBytCntrl	UPDNBBytCntrl	RSTBBytCntrl	RSTBBitCntrl	Sheet-SMCL	CUEXIT1	RSTStatCntrl	RSTStfData	ENStfData	ENStatCntrl
RESET STAT	X	X	X	X	X	X	X	X	X	1	0	0	0	0	0	0	0	1	1	1	0	1	0	0	0
GO STAT	X	X	X	X	X	X	X	X	X	1	0	0	0	0	0	0	1	0	0	1	0	0	0	0	
READ DATAIN STAT	X	1	X	X	X	X	X	X	X	0	0	0	0	0	0	1	1	0	0	1	0	0	0	0	
READ DATAIN STAT	X	0	X	X	X	X	X	X	X	1	1	1	0	0	0	0	1	0	0	1	0	0	0	0	
WRITE BITCNTR STAT	X	X	X	X	X	X	X	X	X	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	
JG_SMC STAT	X	X	1	X	X	X	X	X	X	1	0	0	0	1	0	0	1	0	0	0	0	0	0	0	
JG_SMC STAT	X	X	1	0	X	X	X	X	X	1	0	0	0	1	0	0	1	0	0	0	0	0	0	1	
JG_SMC STAT	X	X	0	X	1	X	X	X	X	1	0	0	0	1	0	0	1	0	0	0	0	0	0	0	
JG_SMC STAT	X	X	0	X	0	X	X	X	X	1	0	0	0	1	0	0	1	0	0	0	0	0	0	1	
SHIFT STAT	X	X	X	X	X	X	X	X	X	1	0	1	0	1	0	0	1	0	0	0	0	0	0	1	
SHIFT DATAINOUT STAT	X	X	X	X	X	X	X	X	X	0	0	0	1	0	0	1	0	0	0	0	0	1	1	0	
SHIFT DATAINOUT STAT	X	X	X	X	X	1	X	X	X	0	0	0	1	0	0	0	1	0	0	0	0	1	1	0	
SHIFT DATAINOUT STAT	X	X	X	X	X	X	1	X	X	0	0	0	1	0	1	0	1	0	0	0	0	1	0	0	
SHIFT DATAINOUT STAT	X	X	X	X	X	0	0	X	X	1	0	1	0	1	0	0	1	0	0	0	0	0	0	1	
PAUSEWRITE STAT	X	X	X	X	X	X	0	X	X	1	0	1	0	1	0	0	1	0	0	0	0	0	0	1	
PAUSEWRITE STAT	X	X	X	X	X	X	1	X	X	1	0	0	0	1	0	0	1	0	0	0	0	1	1	0	
CUSMC_SYNC STAT	X	X	X	X	X	X	1	X	X	1	0	0	0	1	1	0	1	0	1	0	0	1	1	0	
CUSMC_SYNC STAT	X	X	X	X	X	X	0	X	X	1	0	1	0	1	0	0	1	0	0	0	0	1	0	0	
RDYTOXRESULT STAT	X	X	X	X	X	X	X	X	X	1	0	0	0	0	1	1	0	1	0	1	0	1	1	0	
WAITHSTRPLY STAT	X	X	X	X	X	X	X	1	X	1	0	0	0	0	1	1	1	0	1	0	0	1	1	0	
WAITHSTRPLY STAT	X	X	X	X	X	X	X	0	1	1	0	0	0	0	1	1	1	0	1	0	0	1	1	0	
WAITHSTRPLY STAT	X	X	X	X	X	X	X	0	1	1	0	0	0	0	1	0	1	0	1	0	1	1	1	0	

FIG. 8



10039934.100301

FIG. 9

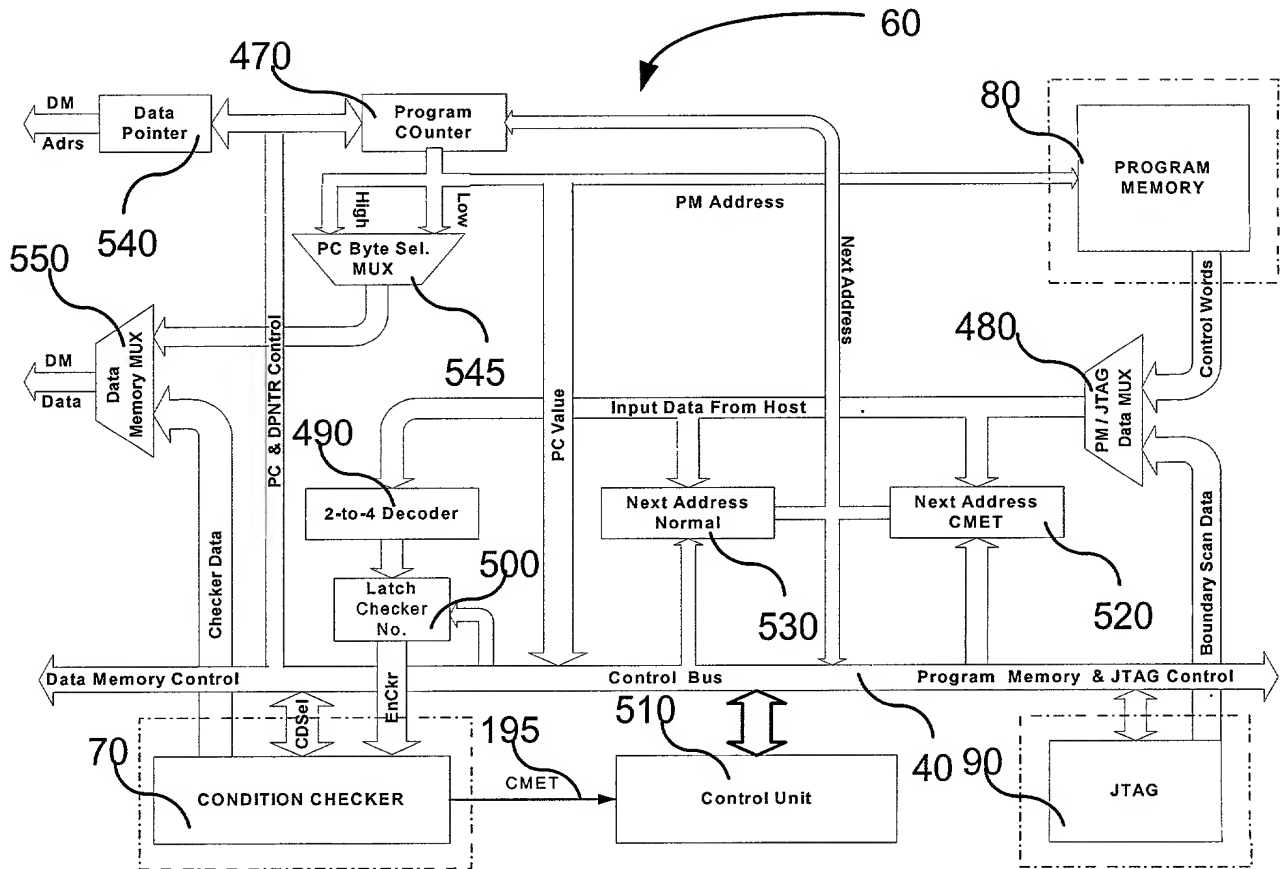


FIG. 10

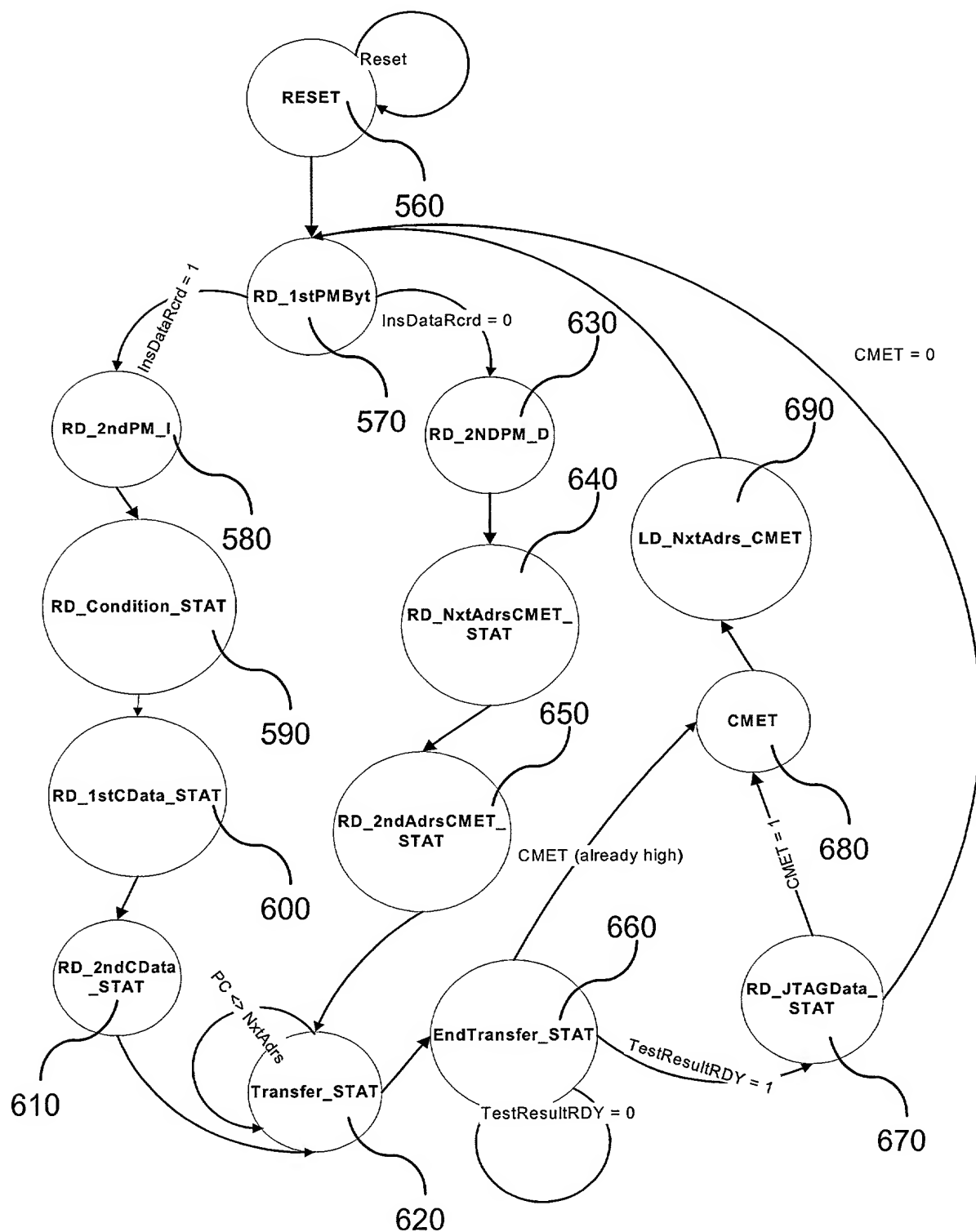
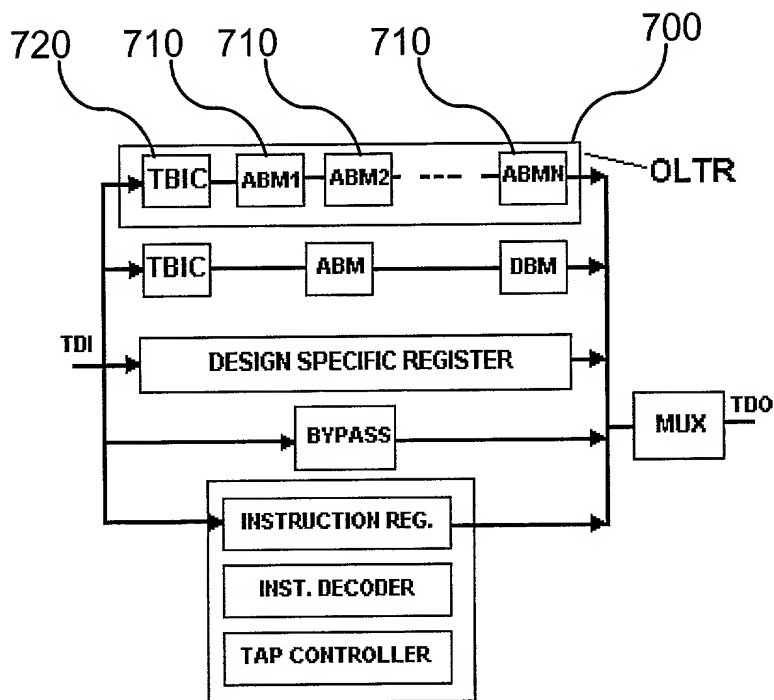


FIG. 10A

STAT / CONTROL OUTPUT	CMET	Next Adrs Nrm1 - LReg + 1 > = p C	Test Input RDY	XmtTYP	DM - OE - Sig	DM - WE - Sig	DM - CE - Sig	PM - OE - Sig	PM - WE - Sig	PM - CE - Sig	PJGD Mux - Sig	ChkFPC - Data Mux - Sig	SReset - ChkR - DQ	CDSelect - Sig	XmtTYP	Test Input RDY	Host RDY	SReset - Dppnt	ENDPnt	
RESET STAT	X	X	X	X	1	0	0	1	1	1	1	0	1	"00"	XmtTyp_Sig	0	0	0	1	0
RD 1stPMemByt	1	X	X	X	1	0	0	1	0	1	0	0	1	"00"	XmtTyp_Sig	0	0	0	0	0
RD 1stPMemByt	0	X	X	X	1	0	0	1	0	1	0	0	1	"00"	XmtTyp_Sig	0	0	0	0	1
RD 2ndPMemByt 1	X	X	X	X	1	1	1	0	1	0	1	1	1	"00"	XmtTyp_Sig	0	0	0	0	0
RD 2ndPMemByt D	X	X	X	X	1	1	1	0	1	0	1	1	1	"00"	XmtTyp_Sig	0	0	0	0	0
RD Condition_STAT	X	X	X	X	1	1	1	0	1	0	1	1	0	CONDITION_TYP	XmtTyp_Sig	0	0	0	0	0
RD 1stCData_STAT	X	X	X	X	1	1	1	0	1	1	1	1	0	CONDITION_1stData	XmtTyp_Sig	1	1	0	0	0
RD 2ndCData_STAT	X	X	X	X	1	1	1	0	1	0	1	1	0	CONDITION_2ndData	XmtTyp_Sig	1	1	0	0	0
Transfer_STAT	X	1	X	X	1	1	1	0	1	0	1	1	1	"00"	XmtTyp_Sig	0	1	0	0	0
Transfer_STAT	X	0	X	X	1	1	1	0	1	0	1	1	1	"00"	XmtTyp_Sig	1	1	0	0	0
EndTransfer_STAT	X	X	1	X	1	1	1	1	1	1	1	1	1	"00"	XmtTyp_Sig	0	0	0	0	0
EndTransfer_STAT	X	X	0		1	1	1	1	1	1	1	1	1	"00"	XmtTyp_Sig	0	1	0	0	0
RD JTAGINSDATA_STAT	X	X	X	1	1	0	0	1	1	1	1	0	1	"00"	XmtTyp_Sig	0	0	1	1	1
RD JTAGINSDATA_STAT	X	X	X	1	1	0	0	1	1	1	1	0	1	"00"	XmtTyp_Sig	0	0	1	0	1
RD JTAGINSDATA_STAT	X	X	X	0	1	0	0	1	1	1	1	0	1	"00"	XmtTyp_Sig	0	0	1	1	1
RD JTAGINSDATA_STAT	X	X	X	0	1	0	0	1	1	1	1	0	1	"00"	XmtTyp_Sig	0	0	1	0	1
CMET_STAT	X	X	X	X	1	1	1	0	1	0	1	1	1	"00"	XmtTyp_Sig	0	0	1	0	0
RD NXAdrsCMet_STAT	X	X	X	X	1	1	1	0	1	0	1	1	1	"00"	XmtTyp_Sig	1	1	0	0	0
RD 2ndNXAdrsCMet_STAT	X	X	X	X	1	1	1	0	1	0	1	1	1	"00"	XmtTyp_Sig	1	1	0	0	0
RD 2ndNXAdrsCMet_STAT	X	X	X	X	1	1	1	0	1	0	1	1	1	"00"	XmtTyp_Sig	0	0	0	0	0

FIG. 11



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